

METHOD AND APPARATUS FOR SPARKLE REDUCTION BY REACTIVE AND ANTICIPATORY SLEW RATE LIMITING

ABSTRACT

A circuit (10) for reducing errors due to adjacent pixel interdependence in a liquid crystal display includes a decomposer (12) for dividing an input signal into a plurality of signals having at least a high brightness signal and at least one low brightness signal, a delay match circuit (14) for the high brightness signal to provide a delay high brightness signal, one or more transient conditioner circuits (16 and/or 18) for processing the at least one low brightness signal to provide at least one transient conditioned low brightness signal, and a combiner (20) for combining the delay matched high brightness signal with the at least one transient conditioned low brightness signal to provide an output signal having reduced sparkle artifacts.

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